Docket Number (Optional)

PTO/SB/51 (12-97)
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## REISSUE APPLICATION DECLARATION BY THE INVENTOR

501.32049R00

As a below named inventor, I hereby declare that:  My residence, post office address and citizenship are stated below next to my name.  I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is described and claimed in patent number 5,637,913 , granted June 10, 1997 , and for which a reissue patent is sought on the invention entitled Leadframe Semiconductor Integrated Circuit Device Using The Same, And Method Of And Process For Fabricating The Two
the specification of which
is attached hereto.
X was filed on June 9, 1999 as reissue application number 09 / 328,910
and was amended on (If applicable)
( <del>S.F.</del> )
I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.  I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.  I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)
by reason of a defective specification or drawing.
X by reason of the patentee claiming more or less than he had the right to claim in the patent.
by reason of other errors.
At least one error upon which reissue is based is described as follows:
Patentee claimed less than he had the right to claim as evidenced by the patented claims 1-14 being each directed to a "semiconductor integrated circuit device", whereas any of added claims 15-36 correct such oversight and claim more by each being alternatively directed to a "method of manufacturing a semiconductor device."

[Page 1 of 2]

Burden Hour Statement: This form is estimated to take 0.5 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

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(REISSUE APPLICATION	DECLARATION BY THE INVENTOR	R, page 2)	Docket Number (Optional) 501.32049R00		
applicant As a named i	nis reissue application arose withon nventor, I hereby appoint the follons assact all business in the Patent and	wing attomey(s	<ul><li>and/or agent(s)</li></ul>	to prosecui	te
Donald R. Antonelli, Reg.	No. 20,296; David T. Terry, Reg. No. 3; Gregory E. Montone, Reg. No. 28,14 Schiavelli, Reg. No. 32,087; James N.	20,178; Melvin K 1: Ronald J. Shor	raus, Reg. No. 22,2 e, Reg. No. 28,577;	66; William Donald E. S	,
Correspondence Address	Direct all communications about the	application to:			7
X Customer Number	020457	$\rightarrow$	Place Customer Number Bar Code Label here		
OR	Type Customer Number here				
Firm or Individual Name					
Address					
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Country					
Telephone		Fax			
plication(s) for patent or in	nefit under Title 35, United States C aventor's certificate listed below as having a filing date before that of the	nd have also id	entified below any	y foreign ap	plication
ovisional and/or Foreign Ap			*	Priority (	
4-320098	Japan		11 /1992	$\mathbf{x}$	
(Number)	(Country)	(Day/M	(Day/Month/Year Filed)		No
4-71116	Japan	27_/	3 / 1992	Yes	
(Number)	(Country)	(Day/M	(Day/Month/Year Filed)		No
		_		. L	Ļ
(Number)	(Country)	(Day/M	Ionth/Year Filed)	Yes	No
elow and, insofar as the su tates application in the man	the benefit under Title 35, United Stablect matter of each of the claims ner provided by the first paragraph information as defined in Title 37 the prior application and the nation	s of this application of Title 35, Un 7 Code of Fede	ation is not discionition is not discionition is not discionition is not discions. }	§112, I/we a §1.56(a) whi	cknowled ch occurr
(Application Serial No.)	(Filling Date)		(Status: patented, per	nding, abandor	red)
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V. F. C			(0)	ding sheader	ned)
(Application Serial No.)	(Filing Date)		(Status: patented, per	namg, avandoi	
(Application Serial No.)	(Filing Date)		(Status: patented, pe	nding, abando	ned)

(REISSUE APPLICATION DECLARATION BY THE INVEN	TOR page 3) Docket Number (Options	:I)				
(NEIGOGE ALL EIGATION DECEMBER TO THE INVENT	501.32049R00	<del></del>				
·						
Full name of sole or first inventor (given name, family name) Yujiro KAJIHARA						
Inventor's signature of unjinou Rajihara	Date T 26: 2000					
Residence	January 21/2					
Same as Post Office Address	Çitizenship Japan					
Post Office Address 3-4-8, Minamino, Hachiou 28-15-401, Shibasaki cho, Tachikawa-shi, Tok	yo, Japan 9. K					
Full name of second joint inventor (given name, family name	9)					
Kazunari SUZUKI						
Inventor's signature Kazunari Suzuki	Date January 26	,2000				
Residence Same as Post Office Address	Citizenship Japan	,				
Post Office Address 4-20-10, Cyuuou, Oota-ku, Tokyo, Japan	J					
Full name of third joint inventor (given name, family name) Kunihiro TSUBOSAKI						
Inventor's signature  Kurihiro Tsuborchi	Date January 29 2000	· <del></del>				
Residence Same as Post Office Address	Citizenship Japan					
Post Office Address 2-29-22, Higashihirayama, Hino-shi, Tokyo, Japan						
Full name of sole or 4thinventor (given name, family name)						
Hiromichi SUZUKI						
Inventor's signature Hiromachi Suzuki	Date January 26, 20	00				
Residence Same as Post Office Address	Citizenship / Japan					
Post Office Address 2-19-5-201, Nishiazabu, Minato-ku, Tokyo, Ja	pan					
Full name of 5th joint inventor (given name, family name Yoshinori MIYAKI	e)					
Inventor's signature Joshimovi Miyaki	Date January 26	. 2000				
Residence	Citizenship					
Same as Post Office Address	Japan					
Post Office Address 1-3-17, Wakaba-cho, Tachikawa-shi, Tokyo, Japan						
Full name of 6th joint inventor (given name, family name) Takahiro NAITO						
Inventorssignature	Date January 21, 2000	1				
Residence Same as Post Office Address	Citizenship Japan					
Post Office Address 3-2-40-305, Gakuenhigashi-cho, Kodaira-shi, Tokyo, Japan						
X Additional joint inventors are named on separately numbered sheets attached hereto.						

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(REISSUE APPLICATION DECLARATION BY THE INVENTOR, page 4)		501.32049R00			
Full name of sole or 7th inventor (given name, family name) Sueo KAWAI					
Inventor's signature Such Kautes					
Residence Same as Post Office Address Post Office Address 1522-49, Ichinoya, Iwama- machi, Nishiibaraki-gun, Ibaraki, Japan Full name of 8th joint inventor (given name, family name	Citizenship Japan	4 20, 2000			
Inventor's signature	Date .				
Residence	Citizenship				
Post Office Address					
Full name of 9th joint inventor (given name, family name)					
Inventor's signature	Date				
Residence	Citizenship				
Post Office Address					
Additional joint inventors are named on separately numbered sheets attached hereto.					

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: KAJIHARA et al.

Application: 09/328,910

(Reissue Application of U.S. Patent 5,637,913; issued June 10, 1997)

Filed: June 9, 1999

For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

USING THE SAME, AND METHOD OF AND PROCESS FOR

**FABRICATING THE TWO** 

Art Unit: 2811

Examiner: Unassigned (parent application's Examiner: J. Clark)

CERTIFICATE UNDER 37 CFR §3.73

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Hitachi, Ltd., a corporation located at 6, Kanda Surugadai 4-chome, Chiyoda-ku,
Tokyo, Japan, and Hitachi ULSI Systems Co., Ltd. (Formerly named Hitachi
Microcomputer System Ltd.), a corporation located at 22-1, Josuihoncho 5-chome,
Kodaira-shi, Tokyo, Japan certifies that they are joint assignees
of the entire right, title and interest in the patent identified above by virtue of an
Assignment (copy attached) which has been recorded in the U.S. Patent and Trademark
Office at Reel 7070, Frame 578-579, and by virtue of a Change of Name document
submitted concurrently herewith (under separate cover letter) for recordation (i.e., the
Change of Name pertaining to a change in corporate name from "Hitachi Microcomputer
System Ltd." to "Hitachi ULSI Systems Co., Ltd.")

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered and authorized to sign this certificate and consent on behalf of the assignee.

As stated also within the Form PTO/SB/53 filed concurrently herewith, the undersigned assignee of the entire interest in the patent identified above, hereby consents to the accompanying reissue application.

I declare that all statements made herein of my own knowledge are true, and that all statements on information and belief are believed to be true, and further state that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United

States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: Fieb. 10.2000

Date: feb 8, 2000

Hitachi, Ltd.

Mr. Yasuo Sakuta

Executive Managing Director and

Patent Attorney

Intellectual Property Group (Authorized Signing Officer)

Hitachi ULSI Systems Co., Ltd.

<u>Junchu Syriki</u> Mr. Jinichiro Suzuki

TITLE: President

(Authorized Signing Officer)

Attachment:

copy of original Assignment copy of cover letter and documents regarding Change of Name